

SUPPLEMENTAL DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name,

I believe I am the original, first and joint inventor of the subject matter claimed and for which a patent is sought on the invention entitled:

SYSTEM FOR FABRICATING AN INTEGRATED CIRCUIT PACKAGE ON A PRINTED CIRCUIT BOARD

of

the specification of	which				
is attached here	to				
\boxtimes was filed on <u>Ap</u> $\underline{N/A}$ (if applica		pplication Serial No. <u>10/825,8</u>	10 and was	s amended on	
I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.					
America before this the same was not in prior to this applica	s invention thereon public use or or ation. I acknowle	the same was ever known or to of or more than one year prior in sale in the United States of dedge the duty to disclose informaccordance with title 37, C	to this app America mormation w	olication, and to ore than one you	hat ear to
or Section 365(b) of 365(a) of any PCT the United States,	f any foreign app international app listed below and	its under Title 35, United Starplication(s) for patent or inveolication which designated at have also identified belowing a filing date before that of	ntor's certi least one c any foreig	ficate, or Section ountry other the application	ion nan for
Prior Foreign App	lication(s):		Dollar 14	C1 · 1	
Number	Country	Day/Month/Year filed	Yes Yes	Claimed <u>No</u>	
I hereby claim the b application(s) listed		JSC 119(e) of any United Star	tes provisio	onal	
Prior Provisional A	Application(s):				
Application Numb	-	<u>g Date</u>			

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I

Application Serial No.: 10/825,810

Docket No.: 27-012

acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. Application(s):

Serial No.

Filing Date

Status: Patented, Pending, Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby revoke any previous Powers of Attorney and appoint the following attorney(s) and/or agent(s), each said individual being a member or associate of The Law Offices of Mikio Ishimaru or being employed by ST Assembly Test Services Ltd.:

Mikio Ishimaru

Reg. No. 27,449

William D. Zahrt, II

Reg. No. 26,070

Justin W. Zahrt

Reg. No. 55,754

for so long as they remain with such law offices or company with full power of substitution and revocation, to prosecute this application and to transact in connection therewith all business in the Patent and Trademark Office and before competent International Authorities; said appointment to be to the exclusion of myself and my other attorney(s); and all future correspondence should be addressed to:

> Mikio Ishimaru The Law Offices of Mikio Ishimaru 1110 Sunnyvale – Saratoga Rd., Suite A1 Sunnyvale, California 94087

Inventor's signature:

Date: Ju (16, 2004

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